



PATENT APPLICATION  
Doc. No. 8750-019

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Chang-Young Lee et al.

Serial No. 10/002,542

Examiner: Thong Quoc Le

Confirmation No. 3270

# 11B Aut  
M. Benson  
3/21/03

Filed: November 13, 2001

Group Art Unit: 2818

For: SEMICONDUCTOR MEMORY DEVICE HAVING SENSE  
AMPLIFIERS AND THE METHOD OF CONTROLLING THE SAME

BOX FEE AMENDMENT  
Assistant Commissioner for Patents  
Washington, D.C. 20231

RESPONSE TO OFFICE ACTION

Responsive to the Office Action, dated November 21, 2002, please amend the application as follows.

03/17/2003 00000001 00000003 10000000

01 F021503  
02 F021201

\$0.00 CP  
\$4.00 CP

IN THE CLAIMS

A full set of pending claims that incorporates all amendments appears below.

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1. (Twice Amended) A circuit, comprising:  
a read charge control circuit activated only during read operations by a read signal and an address; and  
a write charge control circuit activated by a write signal and the same or a different address, the read charge control circuit and the write charge control circuit both coupled to a first and a second data IO line, wherein the second data IO line is complementary to the first data IO line.
2. The circuit according to claim 1 wherein the read charge control circuit is a sense amplifier.